

The following figures illustrate conceptual details of parts of the overall resource-address-translation flow.

ART_Example	AID=0000	AID=0001	AID=0002		AID=FFF
	ART_Example				
]	
]	
		•			
	[•	•		•
		•	·	· .	
				1	

Adapter-Type / Adapter-ID to Adapter-Resource Table Address

FIG 2A

RT	ART_Example
00	ART Entry ₀
01	ART Entry ₁
02	ART_Example Entry for CQ Resource Type
03	
•	·
FF	ART Entry ₂₅₅

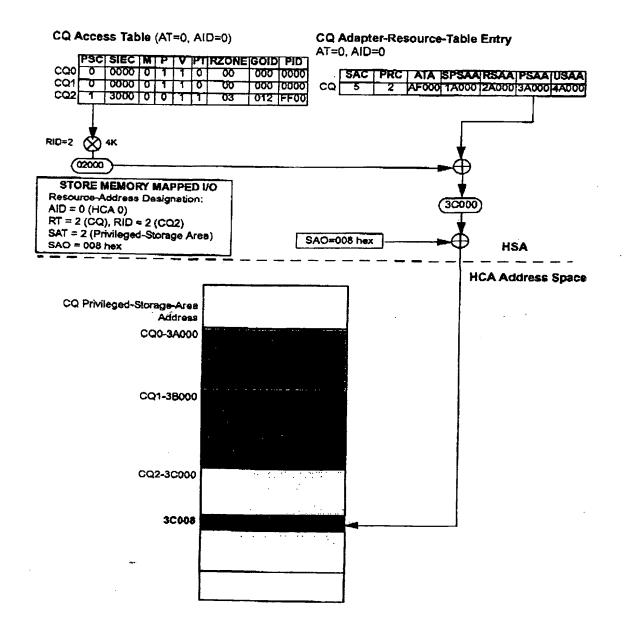
FIG. 28

Adapter Resource Table (ART)

0	AS	MRC					
1	SAC	SAC Reserved SMC Reserve					
2	Reserved	Reserved LARID Reserved					
3 [Reserved				
4	Adapter-Storage Base Address for SAT=0						Reserved
5	Adapter-Storage Base Address for SAT=1						Reserved
18			· ·	· .			Reserved
9			Reserved				
7)	8 1	6	32	40	62	6

Figure JC Adapter-Resource-Table Entry (ARTE) .

Figure 3 Resource Address Translation.



 $\mathcal{F}_{\mathrm{pos}}^{\mathrm{tr}} = \{ (\mathbf{e}_{\mathrm{pos}}^{\mathrm{tr}}) : (\mathbf{e}_{\mathrm{pos}}^{\mathrm{tr}}) \in \mathcal{A}_{\mathrm{pos}}^{\mathrm{tr}} \in \mathbb{R}^{n} \mid \mathbb{R}^{n} \}$